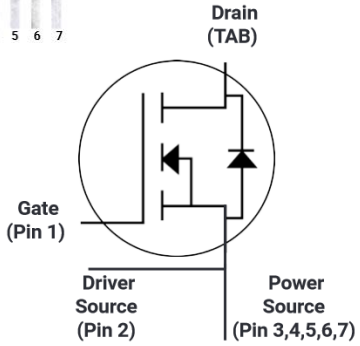
Silicon Carbide Power MOSFET （N-Channel Enhancement）

V_{DS}	650V
$I_{D\ (25°C)}$	57A
$R_{DS(on)}$	60mΩ



Features

- High speed switching
- Essentially no switching losses
- Reduction of heat sink requirements
- Maximum working temperature at 175 °C
- High blocking voltage
- Fast Intrinsic diode with low recovery current
- High-frequency operation
- Halogen free, RoHS compliant
- AEC-Q101 qualified

Typical Applications

Typical applications are in power factor correction(PFC), solar inverter, uninterruptible power supply, motor drives, photovoltaic inverter, electric car and charger.

Mechanical Data

- **Package**: TO263-7L
- **Terminals**: Tin plated leads
- **Polarity**: As marked

■Maximum Ratings (T_C=25℃ Unless otherwise specified）

PARAMTETER	SYMBOL	UNIT	VALUE	TEST CONDITIONS	NOTE
Device marking code			D206560B7GH		
Drain source voltage @ T_j=25°C	$V_{DS,max}$	V	650	V_{GS}=0 V, I_D=100uA	
Gate source voltage @ T_j=25°C	$V_{GS,max}$	V	-10/+22	Absolute maximum values (AC f > 1Hz, duty cycle < 1%)	Note1
Gate source voltage @ T_j=25°C	$V_{GS,op}$	V	-5/+18	Recommended operational values	
Continuous drain current @ T_c=25°C	I_D	A	57	V_{GS}=18V, Tc=25℃	Fig.14
Continuous drain current @ T_c=110°C			38	V_{GS}=18V, Tc=110℃	
Pulse Drain Current	$I_{D,\ pulse}$	A	127	Limited by t_{pw}	Fig.15
Avalanche energy, Single Pulse	E_{AS}	mJ	1600	V_{DD}=100V, I_D=10A	
Power Dissipation	P_{TOT}	W	223	T_c=25°C , Tj = 175℃	Fig.13
Operating junction and Storage temperature range	Tj ,Tstg	°C	-55 to +175		
Soldering temperature	T_L	°C	260	1.6mm (0.063") from case for 10s	
Mounting torque	T_M	Nm	0.6	M3 screw Maximum of mounting process: 3	

■Static Electrical Characteristics（Tc=25℃ unless otherwise specified ）

PARAMTETER	SYMBOL	UNIT	Min.	Typ.	Max.	Test Conditions	Note
Gate threshold voltage	$V_{GS(th)}$	V		2.6		V_{DS}=V_{GS}, I_D= 20mA	Fig.4, 11
Drain source breakdown voltage	$V_{(BR)DSS}$	V	650			V_{GS}=0, I_D=100uA	
Zero gate voltage drain current	I_{DSS}	uA		<1	100	V_{DS}=650V, V_{GS}= 0V	
				10	500	V_{DS}=650V, V_{GS}= 0V, Tj= 175℃	
Gate source leakage current	I_{GSS}	nA			250	V_{GS}= 18V, V_{DS}=0V	
Current drain source on-state resistance	$R_{DS\ ON}$	mΩ		60	75	V_{GS}=18V, I_D=20A	Fig.3, 5, 6
				75		V_{GS}=18V, I_D=20A,Tj=175℃	
Transconductance	g_f	S		13.2		V_{DS}=15V,I_D=40A	

■Dynamic Electrical Characteristics（Tc=25℃ unless otherwise specified ）

PARAMTETER	SYMBOL	UNIT	Min.	Typ.	Max.	Test Conditions	Note
Input capacitance	Ciss	pF		1850		V_{DS}=400V, V_{GS}=0V, Tj=25℃, f=1MHz, V_{AC}= 25mV	Fig.10
Output capacitance	Coss			205			
Reverse capacitance	Crss			33			
Coss stored energy	Eoss	uJ		23			Fig.12
Gate source charge	Qgs	nC		30		V_{DS}=400V, V_{GS}=-5/18V, I_D=30A	Fig.16
Gate drain charge	Qgd			43			
Gate charge	Qg			116			
Internal Gate Resistance	$R_{G(int)}$	Ω		1.2		f =1MHz, V_{AC} = 25mV	

■Switching Characteristics（Tc=25℃ unless otherwise specified ）

PARAMTETER	SYMBOL	UNIT	Min.	Typ.	Max.	Test Conditions	Note
Turn on delay time	$t_{d(on)}$	ns		15		V_{DD}=400V, V_{GS}=-4/+18V, I_D=20A, R_L=20Ω, $R_{G(ext)}$= 2.7Ω	
Rise time	t_r			16			
Turn off delay time	$t_{d(off)}$			19			
Fall time	t_f			9			
Turn on switching energy	Eon	uJ		20		V_{DD}=400V, V_{GS}=0/+18V, I_D=50A, $R_{g(ext)}$=2.7Ω	Fig.17, 18
Turn off switching energy	Eoff			27			

■Body diode characteristics（Tc=25℃ unless otherwise specified ）

PARAMTETER	SYMBOL	UNIT	Min.	Typ.	Max.	Test Conditions	Note
Diode forward voltage	V_{SD}	V		3.0		V_{GS}=0V,I_{SD}=5A	Fig.8
Continuous diode forward current	I_S	A		39		V_{GS}=0V,Tc=25℃	
Reverse recovery time	trr	nS		58		V_{DS}=400V, V_{GS}=0V, I_{SD}=30A, di/dt=300A/uS	
Reverse recovery charge	Qrr	nC		122			
Peak reverse recovery current	Irrm	A		3.75			

Note 1: When using SiC Body Diode the maximun recommended V_{GS} = -5V

■Thermal Characteristics （T_a=25℃ Unless otherwise specified）

PARAMETER	SYMBOL	UNIT	Value
Thermal resistance	$R_{\theta J\text{-}C}$	°C /W	0.67

■Typical Characteristics

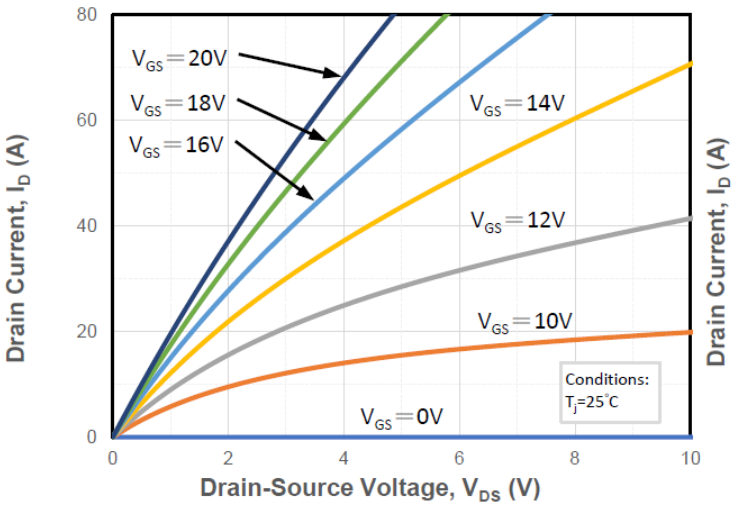


Figure 1. Output Characteristics Tj = 25℃

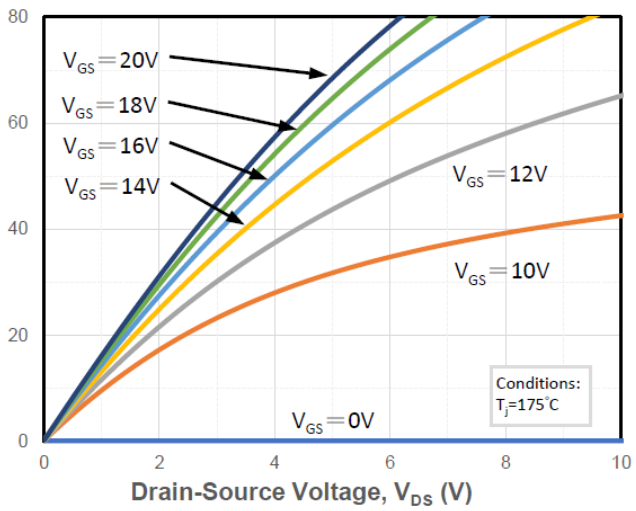


Figure2. Output Characteristics Tj = 175℃

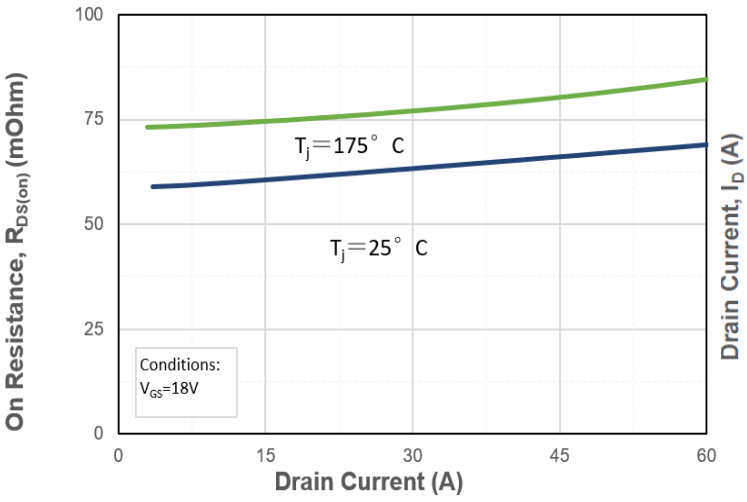


Figure 3. On-resistance vs. drain current

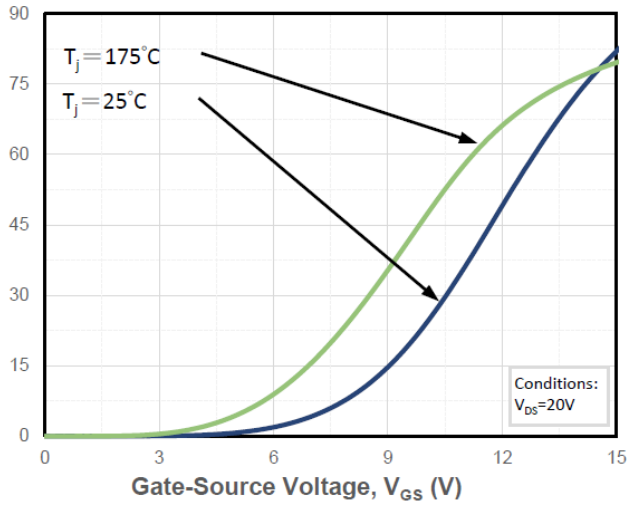


Figure 4. Transfer Characteristics for various Tj

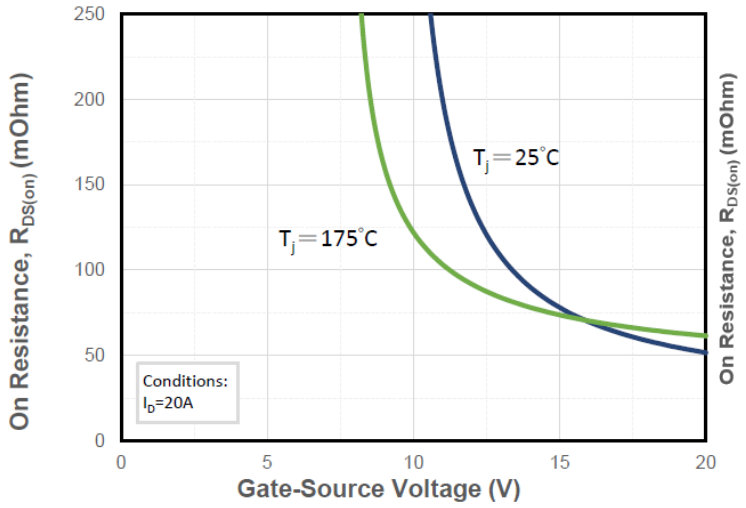


Figure 5. On-resistance vs. gate voltage for various Tj

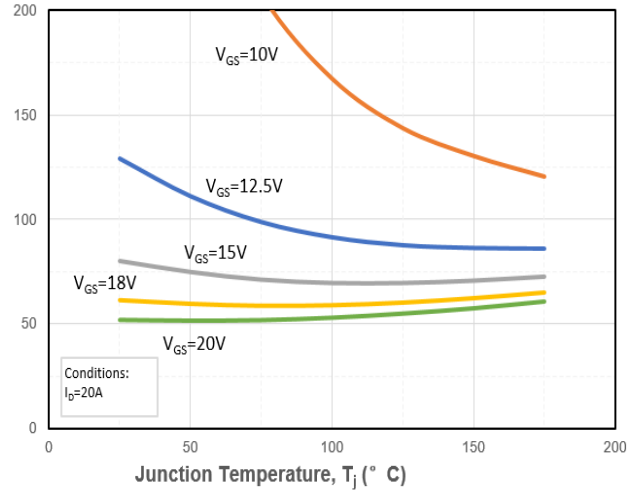


Figure 6. On-resistance vs. Temperature for various Gate voltage

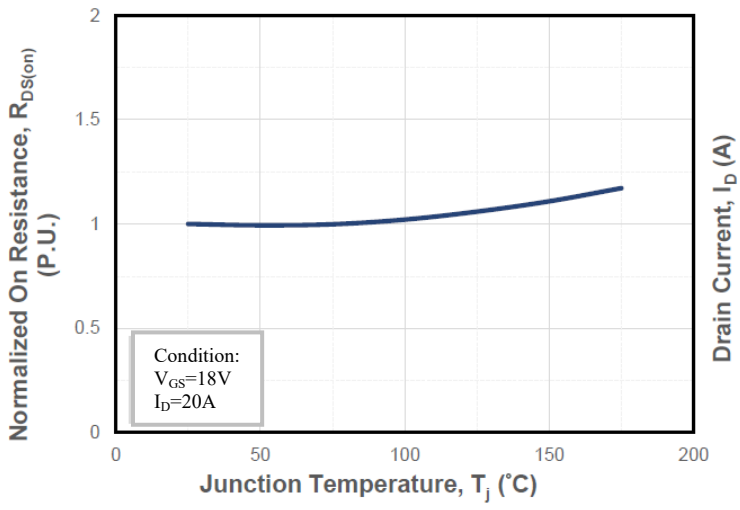


Figure 7. Normalized On-Resistance vs.Temperature

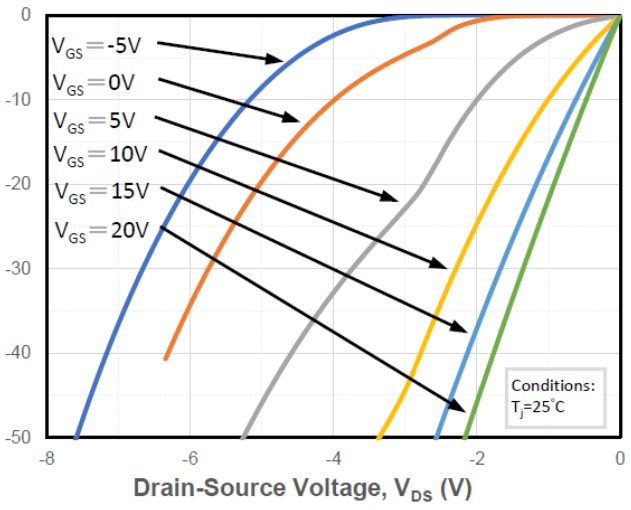


Figure 8. Reverse Output Characteristics at Tj = 25°C

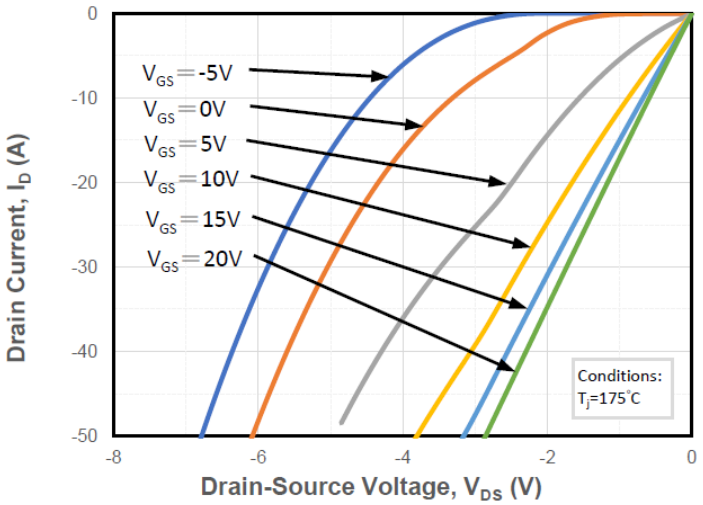


Figure 9. Reverse Output Characteristics at Tj = 175°C

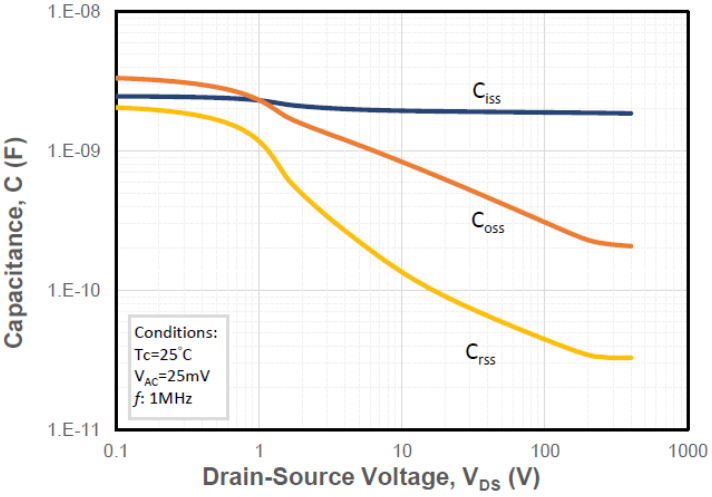


Figure 10. Capacitances vs. Drain to Source Voltage

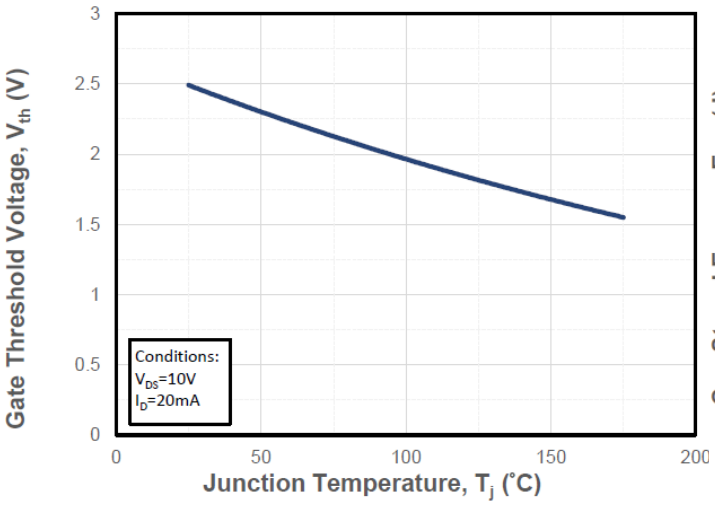


Figure 11. Threshold voltage vs.temperature

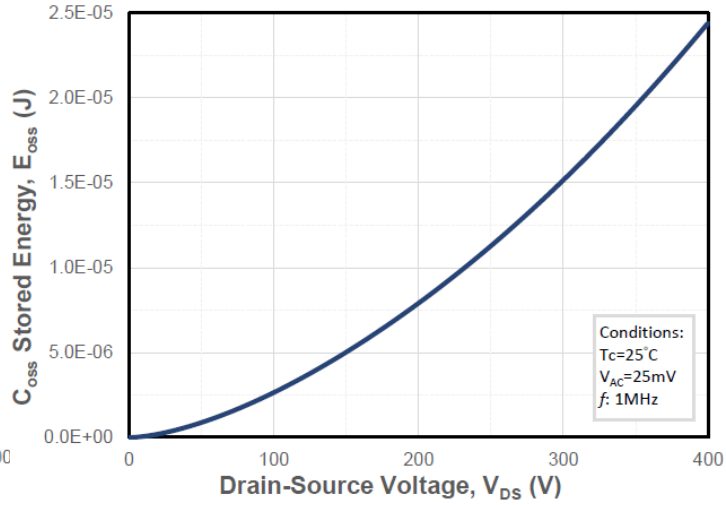


Figure 12. Output Capacitor Stored Energy

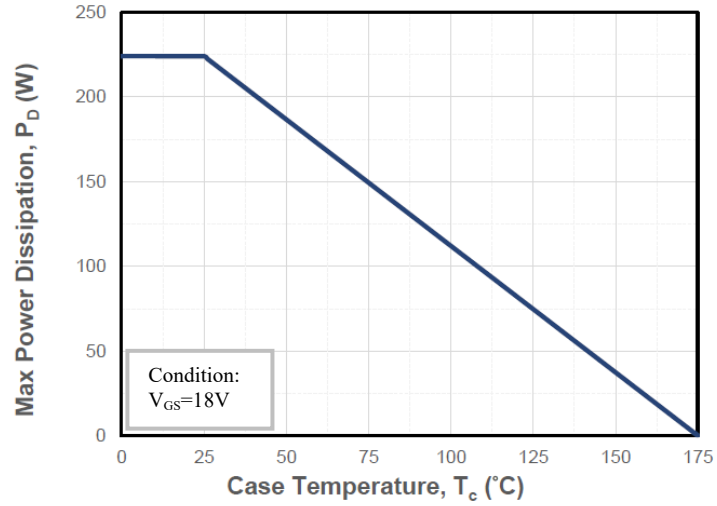


Figure 13. Maximum Power Dissipation Derating vs. Case Temperature

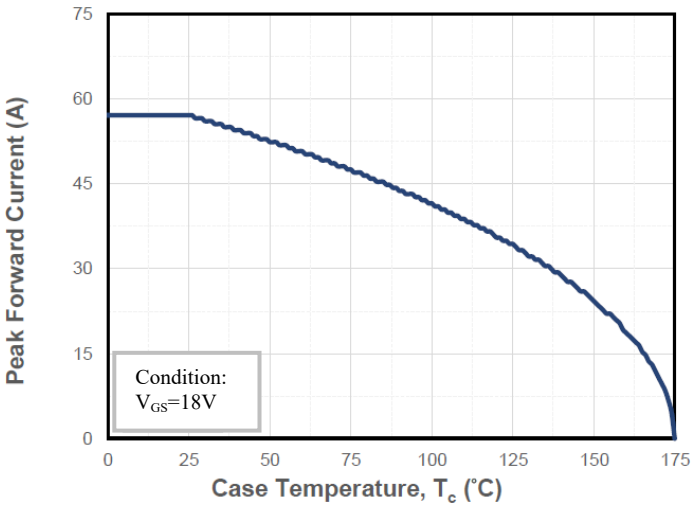


Figure 14. Drain Current Derating vs. Case Temperature

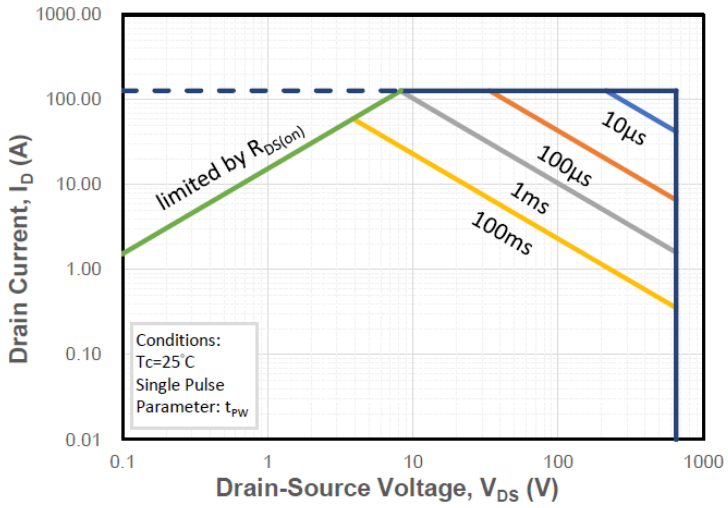


Figure 15. Safe Operating Area

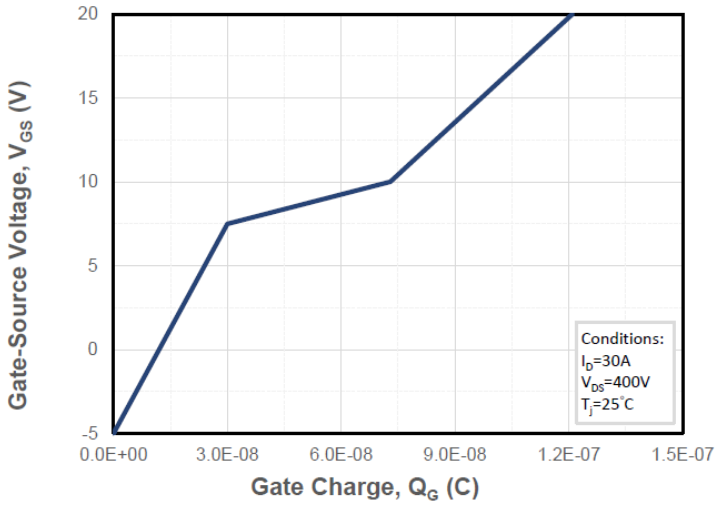


Figure 16. Gate Charge Characteristics

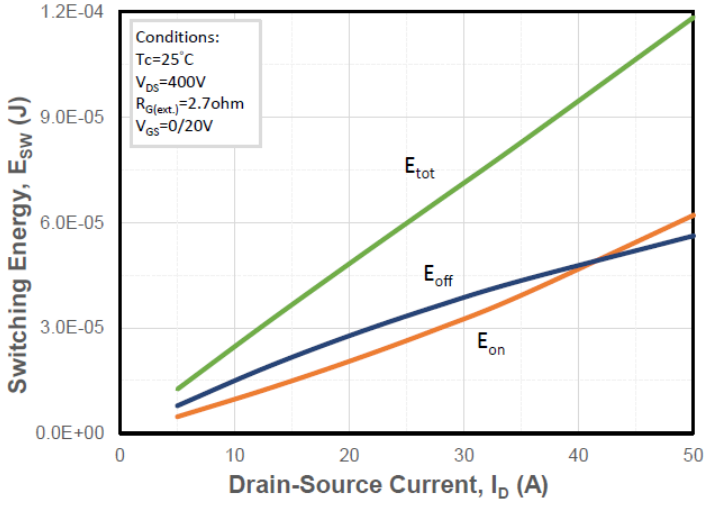


Figure 17. Clamped Inductive Switching Energy vs. Drain Current

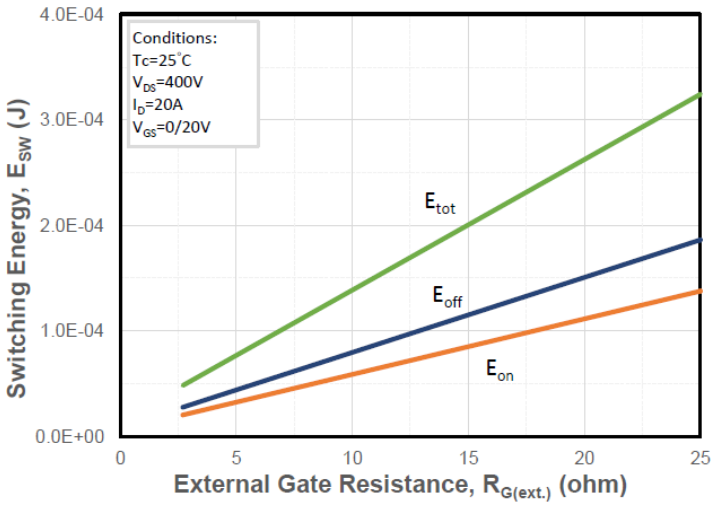


Figure 18. Clamped Inductive Switching Energy vs. External Gate Resistor (R_G(ext.))

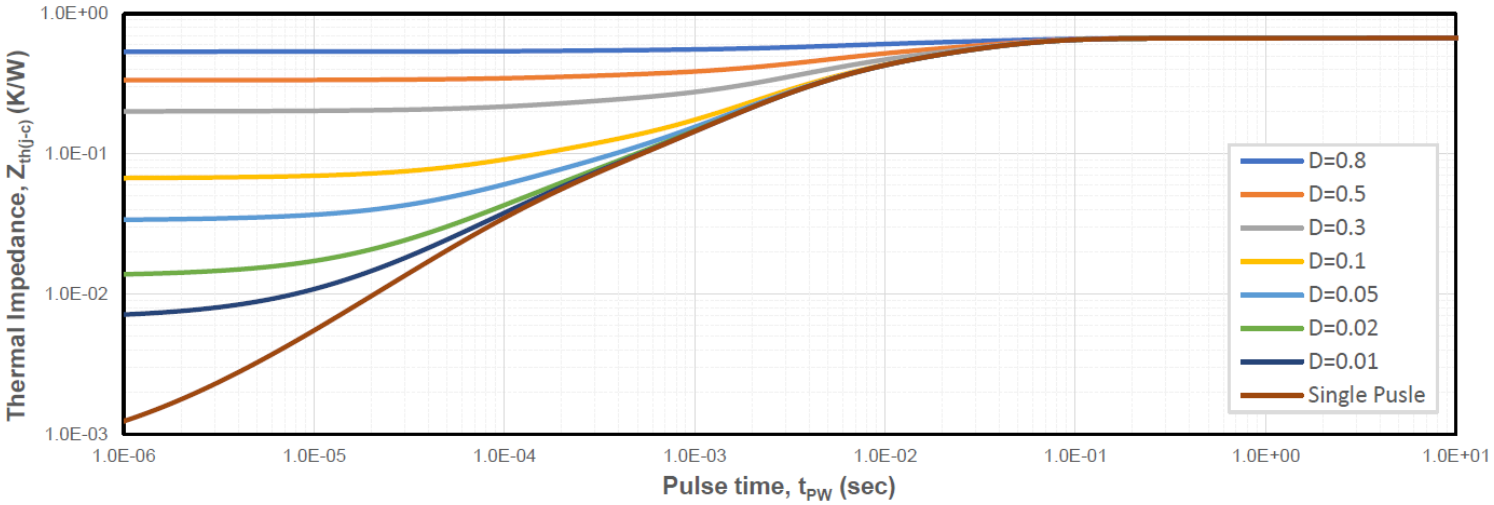


Figure 19. Transient Junction to Case Thermal Impedance

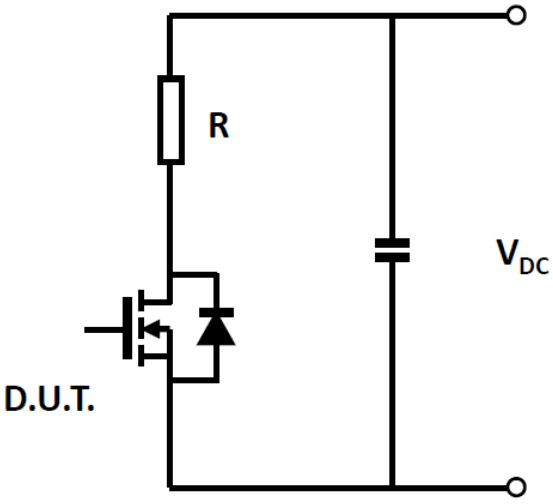


Figure 20. Schematic of Resistive Switching

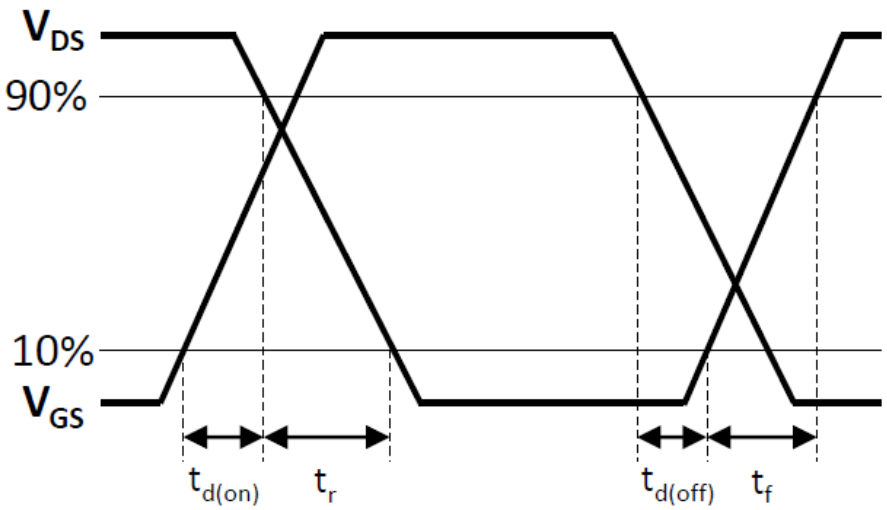
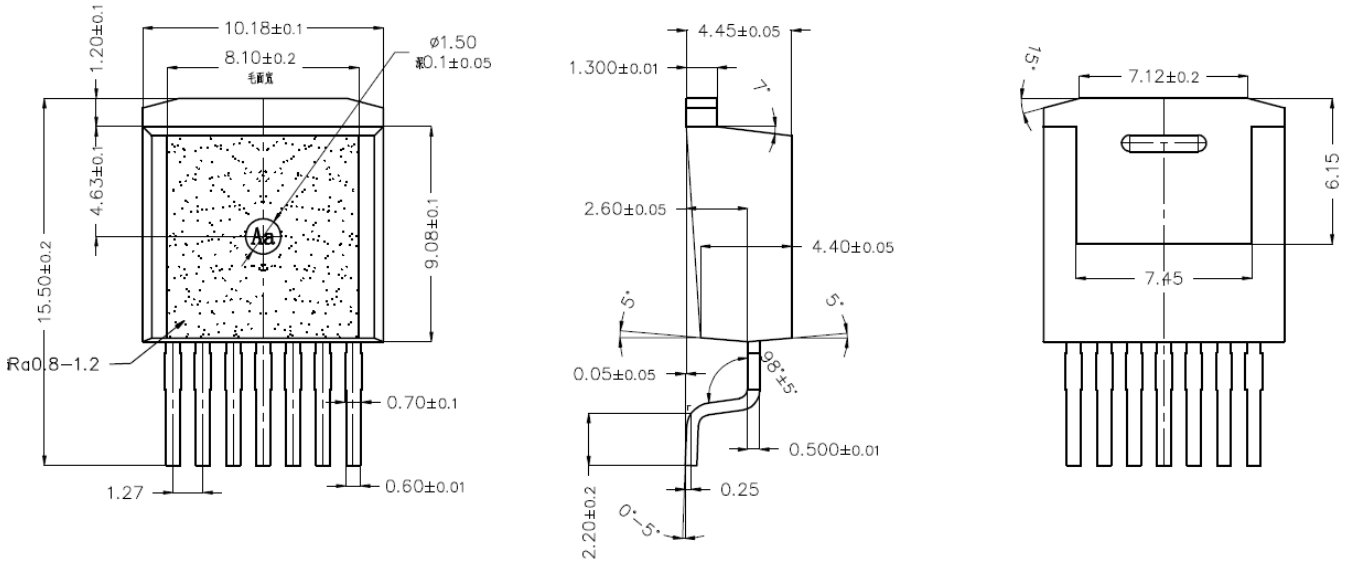


Figure 21. Switching Times Definition

■Outline Dimensions



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