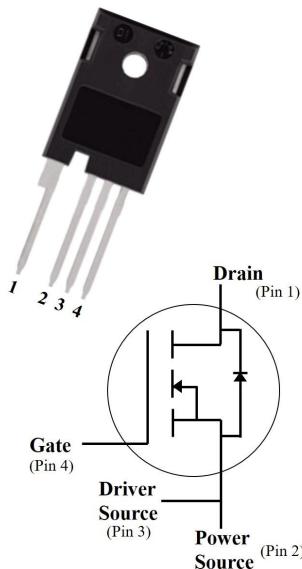


**Silicon Carbide Power MOSFET (N-Channel Enhancement)**

| | |
|-------------------|-------|
| V_{DS} | 1200V |
| $I_D(25^\circ C)$ | 78A |
| $R_{DS(on)}$ | 30mΩ |

**Features**

- High speed switching
- Essentially no switching losses
- Reduction of heat sink requirements
- Maximum working temperature at 175 °C
- High blocking voltage
- Fast Intrinsic diode with low recovery current
- High-frequency operation
- Halogen free, RoHS compliant

Typical Applications

Typical applications are in power factor correction(PFC), solar inverter, uninterruptible power supply, motor drives, photovoltaic inverter, electric car and charger.

Mechanical Data

- **Package:** TO247-4L
- **Terminals:** Tin plated leads
- **Polarity:** As marked

■Maximum Ratings ($T_c=25^\circ C$ Unless otherwise specified)

| PARAMETER | SYMBOL | UNIT | VALUE | TEST CONDITIONS | NOTE |
|--|-----------------|------|-------------|---|--------|
| Device marking code | | | | D212030NCFGH | |
| Drain source voltage @ $T_j=25^\circ C$ | $V_{DS,max}$ | V | 1200 | $V_{GS}=0 V$, $I_D=100\mu A$ | |
| Gate source voltage @ $T_j=25^\circ C$ | $V_{GS,max}$ | V | -10/+25 | Absolute maximum values (AC f > 1Hz, duty cycle < 1%) | |
| Gate source voltage @ $T_j=25^\circ C$ | $V_{GS,op}$ | V | -5/+20 | Recommended operational values | |
| Continuous drain current @ $T_c=25^\circ C$ | I_D | A | 78 | $V_{GS}=20V$, $T_c=25^\circ C$ | Fig.14 |
| Continuous drain current @ $T_c=110^\circ C$ | | | 53 | $V_{GS}=20V$, $T_c=110^\circ C$ | |
| Pulsed drain current | $I_{D(pulsed)}$ | A | 349 | Pulse width tp limited by $T_{j,max}$ | Fig.15 |
| Avalanche energy, Single Pulse | E_{AS} | J | 2.5 | $V_{DD}=100V$, $I_D=14A$ | |
| Power Dissipation | P_{TOT} | W | 375 | $T_c=25^\circ C$, $T_j = 175^\circ C$ | Fig.13 |
| Operating junction and Storage temperature range | T_j, T_{stg} | °C | -55 to +175 | | |
| Soldering temperature | T_L | °C | 260 | 1.6mm (0.063") from case for 10s | |
| Mounting torque | T_M | Nm | 1.0 | M3 screw Maximum of mounting process: 3 | |



■ Static Electrical Characteristics (Tc=25°C unless otherwise specified)

| PARAMETER | SYMBOL | UNIT | Min. | Typ. | Max. | Test Conditions | Note |
|--|----------------------|------|------|------|------|--|-------------|
| Gate threshold voltage | V _{GS(th)} | V | | 2.7 | | V _{DS} =V _{GS} , I _D = 50mA | Fig.4, 11 |
| Drain source breakdown voltage | V _{(BR)DSS} | V | 1200 | | | V _{GS} =0, I _D =100uA | |
| Zero gate voltage drain current | I _{DSS} | uA | | <1 | 50 | V _{DS} =1200V, V _{GS} = 0V | |
| | | | | 10 | 500 | V _{DS} =1200V, V _{GS} = 0V, T _j = 175°C | |
| Gate source leakage current | I _{GSS} | nA | | | 250 | V _{GS} = 20V, V _{DS} =0V | |
| Current drain source on-state resistance | R _{DS ON} | mΩ | | 30 | 40 | V _{GS} =20V, I _D =40A | Fig.3, 5, 6 |
| | | | | 54 | | V _{GS} =20V, I _D =40A, T _j =175°C | |
| Transconductance | g _f | S | | 17 | | V _{DS} =15V, I _D =40A | |

■ Dynamic Electrical Characteristics (Tc=25°C unless otherwise specified)

| PARAMETER | SYMBOL | UNIT | Min. | Typ. | Max. | Test Conditions | Note |
|--------------------------------|---------------------|------|------|------|------|---|--------|
| Input capacitance | C _{iss} | pF | | 4909 | | V _{DS} =800V, V _{GS} =0V, T _j =25°C, f=1MHz, V _{AC} = 25mV | Fig.10 |
| Output capacitance | C _{oss} | | | 198 | | | |
| Reverse capacitance | C _{rss} | | | 34 | | | |
| C _{oss} stored energy | E _{oss} | uJ | | 80.5 | | | Fig.12 |
| Gate source charge | Q _{gs} | nC | | 91 | | V _{DS} =800V, V _{GS} =-5/20V, I _D =40A | Fig.16 |
| Gate drain charge | Q _{gd} | | | 88 | | | |
| Gate charge | Q _g | | | 305 | | | |
| Internal Gate Resistance | R _{G(int)} | Ω | | 0.7 | | f =1MHz, V _{AC} = 25mV | |

■ Switching Characteristics (Tc=25°C unless otherwise specified)

| PARAMETER | SYMBOL | UNIT | Min. | Typ. | Max. | Test Conditions | Note |
|---------------------------|---------------------|------|------|------|------|--|------------|
| Turn on switching energy | t _{d(on)} | ns | | 31 | | V _{DD} =800V, V _{GS} =-4/+20V, I _D =40A, R _L =20Ω, R _{G(ext)} = 2.7Ω | |
| Turn off switching energy | t _r | | | 55 | | | |
| Turn on delay time | t _{d(off)} | | | 8 | | | |
| Rise time | t _f | | | 12 | | | |
| Turn off delay time | E _{on} | uJ | | 167 | | V _{DD} =800V, V _{GS} =0/+20V, I _D =40A, R _{g(ext)} =2.7Ω | Fig.17, 18 |
| Fall time | E _{off} | | | 254 | | | |

■Body diode characteristics (T_c=25°C unless otherwise specified)

| PARAMETER | SYMBOL | UNIT | Min. | Typ. | Max. | Test Conditions | Note |
|----------------------------------|------------------|------|------|------|------|--|-------|
| Diode forward voltage | V _{SD} | V | | 3.0 | | V _{GS} =0V, I _{SD} =10A | Fig.8 |
| Continuous diode forward current | I _s | A | | 50 | | V _{GS} =0V, T _c =25°C | |
| Reverse recovery time | t _{rr} | nS | | 79 | | V _{DS} =400V, V _{GS} =0V, I _{SD} =30A, di/dt=300A/uS | |
| Reverse recovery charge | Q _{rr} | nC | | 284 | | | |
| Peak reverse recovery current | I _{rrm} | A | | 6.8 | | | |

■Thermal Characteristics (T_a=25°C Unless otherwise specified)

| PARAMETER | SYMBOL | UNIT | Value |
|--------------------|-------------------|-------|-------|
| Thermal resistance | R _{θJ-C} | °C /W | 0.4 |

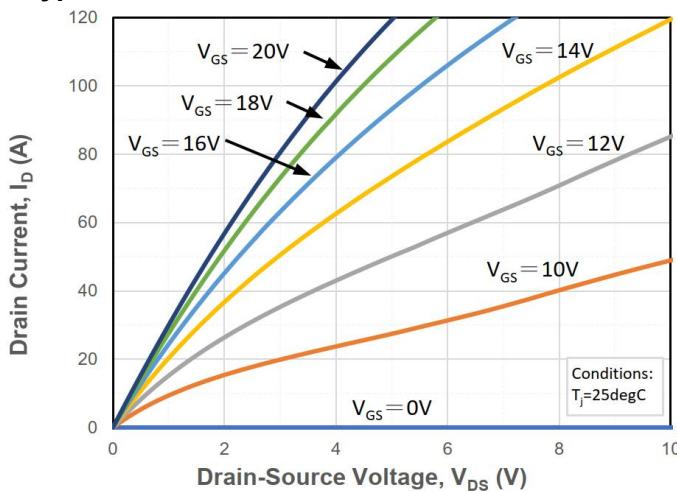
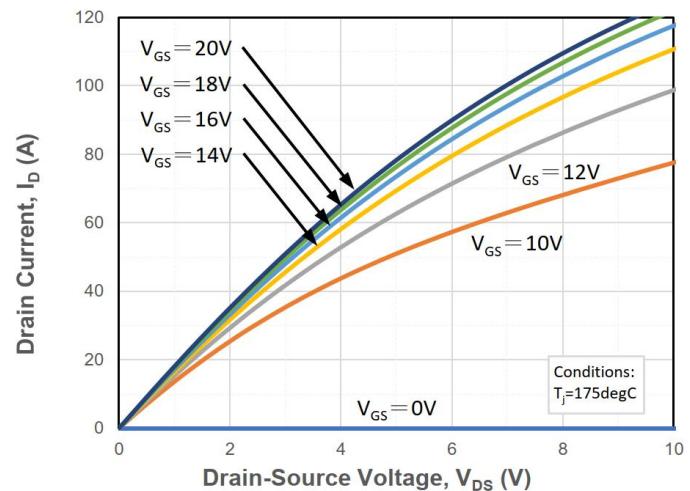
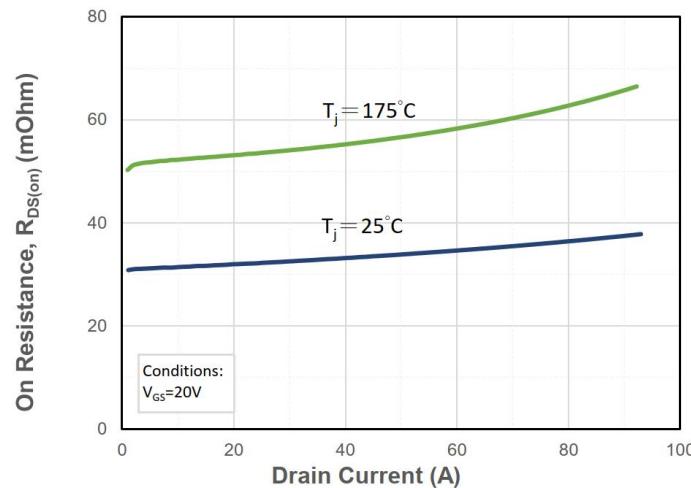
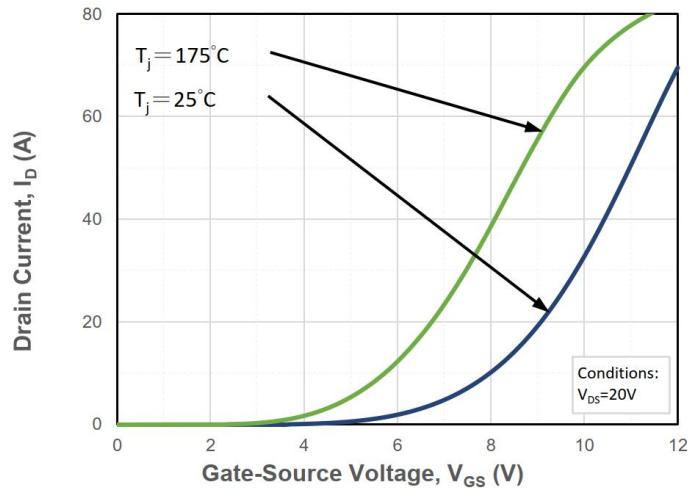
■Typical Characteristics

 Figure 1. Output Characteristics T_j = 25°C

 Figure 2. Output Characteristics T_j = 175°C


Figure 3. On-resistance vs. drain current


 Figure 4. Transfer Characteristics for various T_j

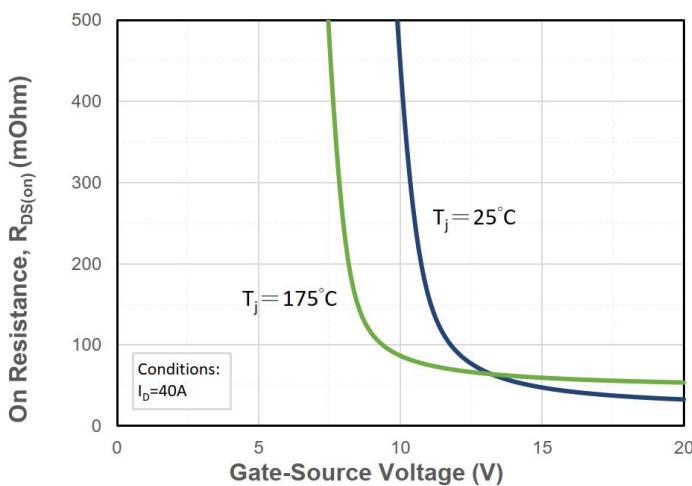


Figure 5. On-resistance vs. gate voltage for various T_j

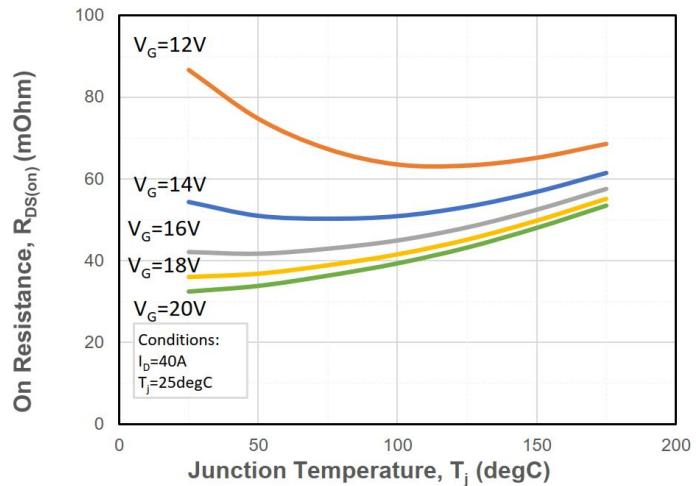


Figure 6. On-resistance vs. Temperature for various Gate voltage

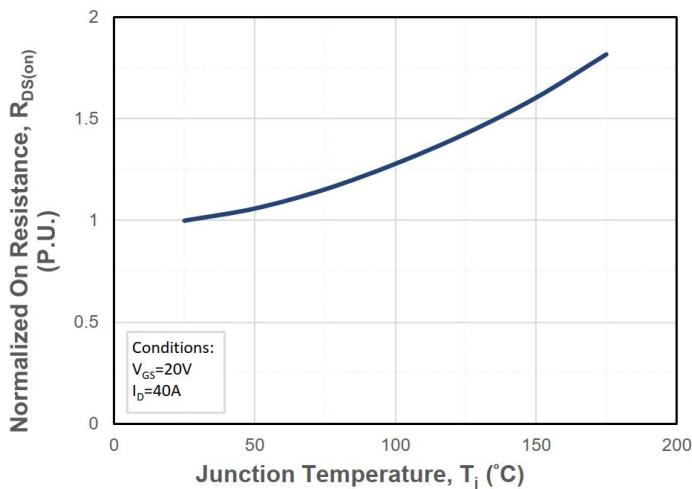


Figure 7. Normalized On-Resistance vs. Temperature

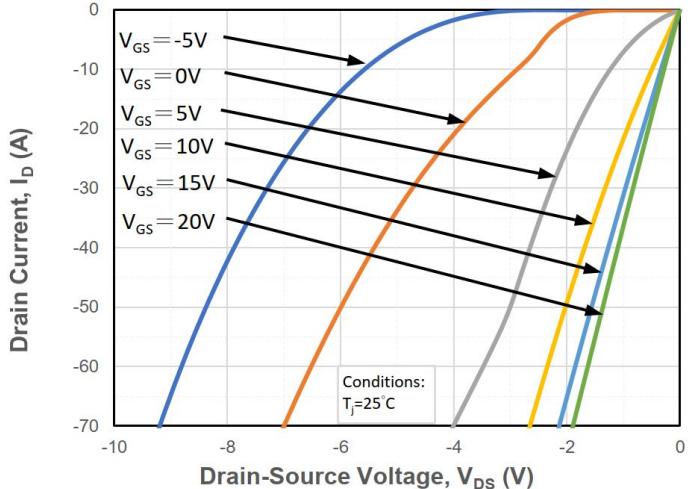


Figure 8. Reverse Output Characteristics at $T_j = 25^\circ C$

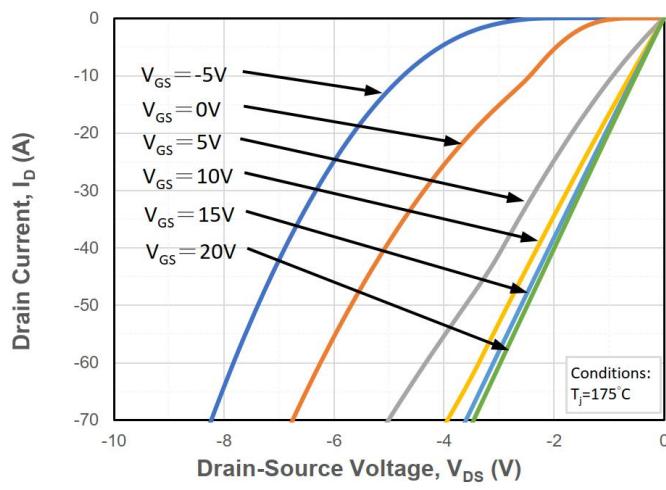


Figure 9. Reverse Output Characteristics at $T_j = 175^\circ C$

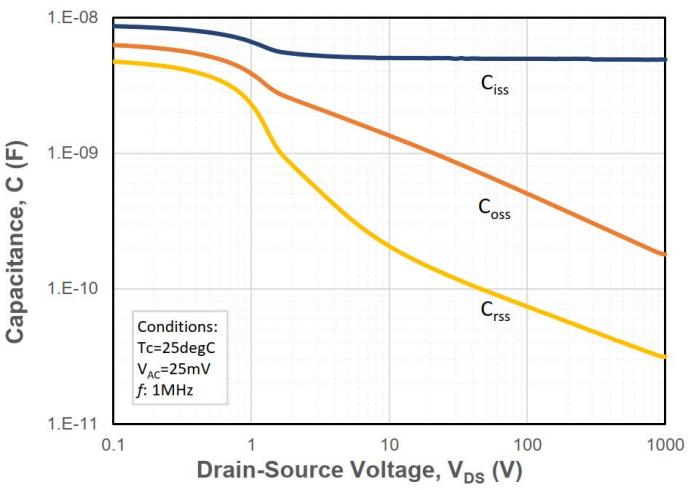
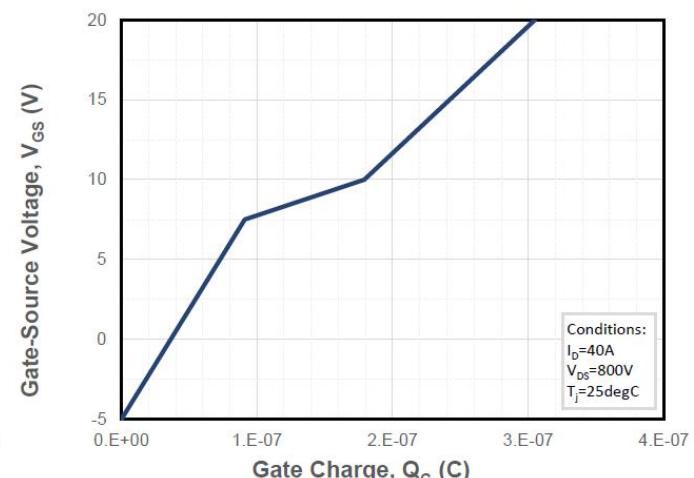
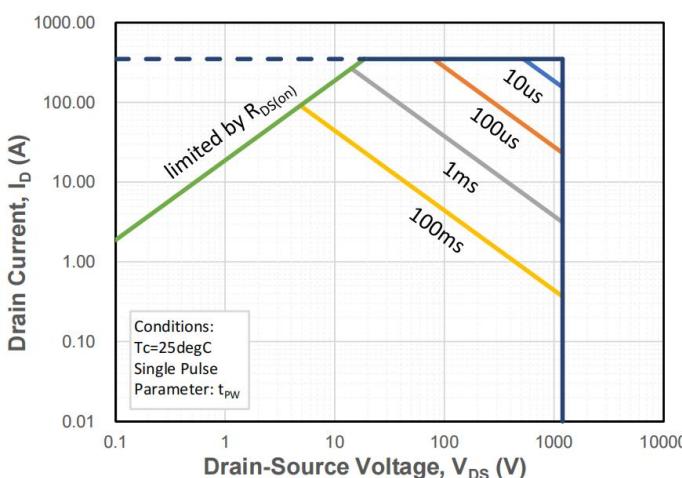
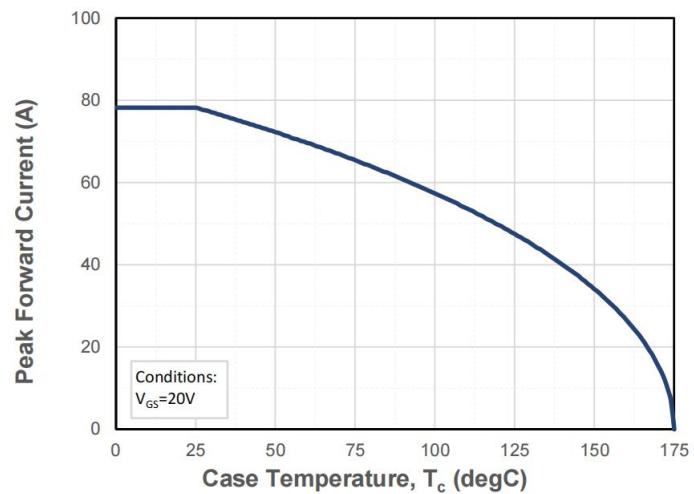
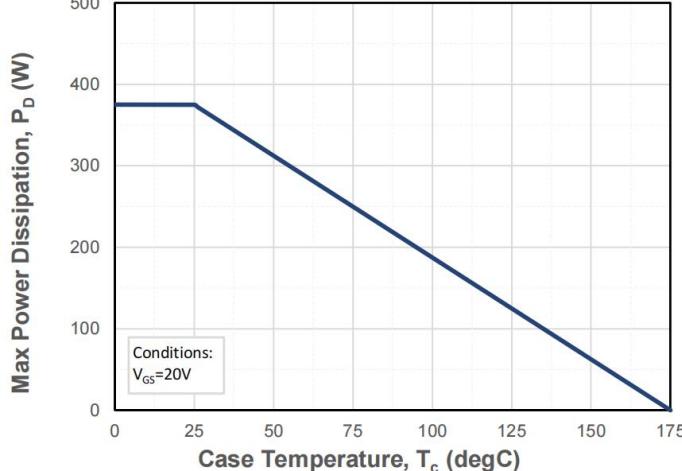
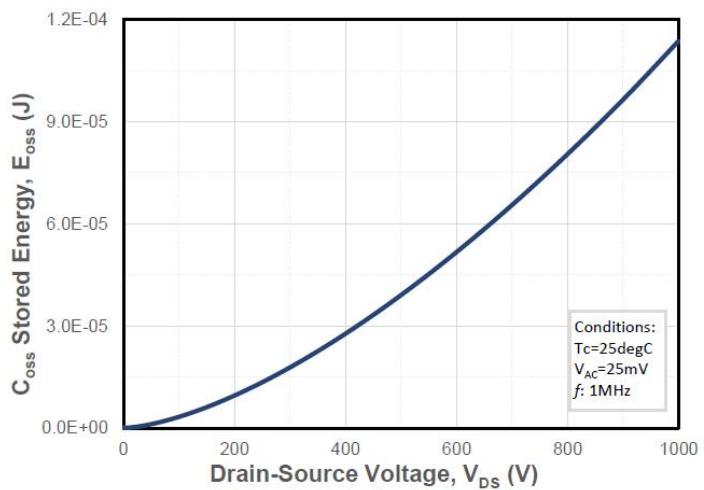
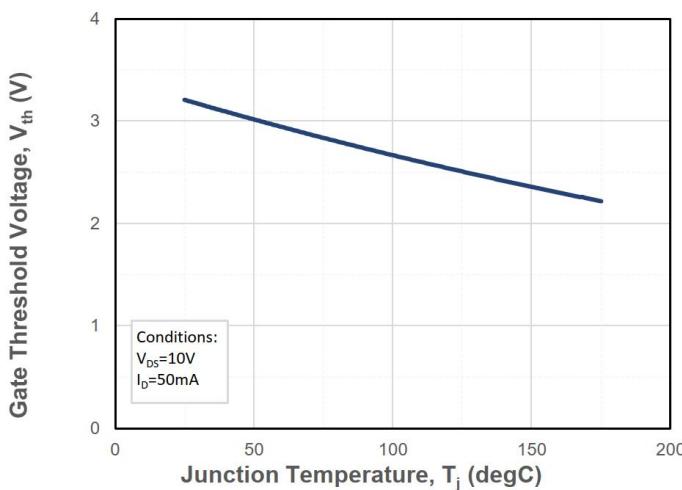


Figure 10. Capacitances vs. Drain to Source Voltage



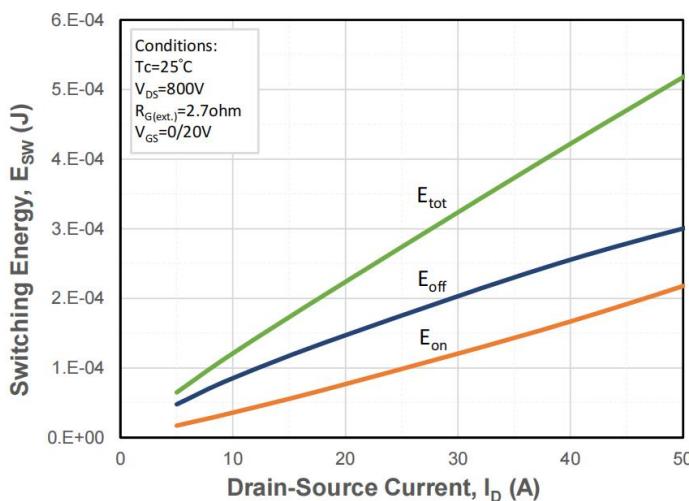


Figure 17. Clamped Inductive Switching Energy vs. Drain Current

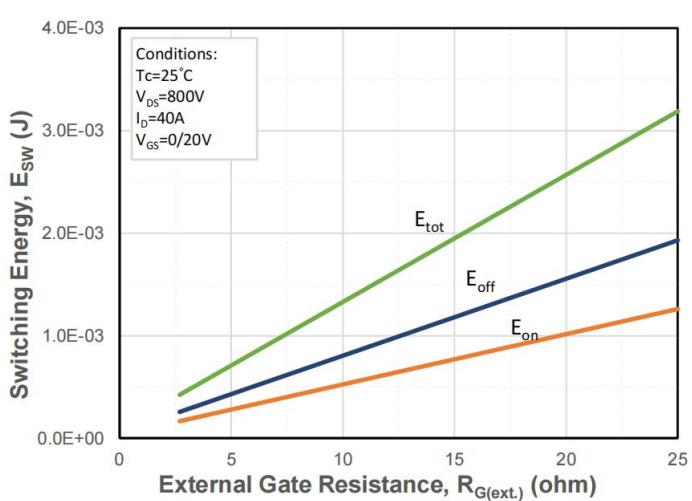


Figure 18. Clamped Inductive Switching Energy vs. External Gate Resistor ($R_{G(ext.)}$)

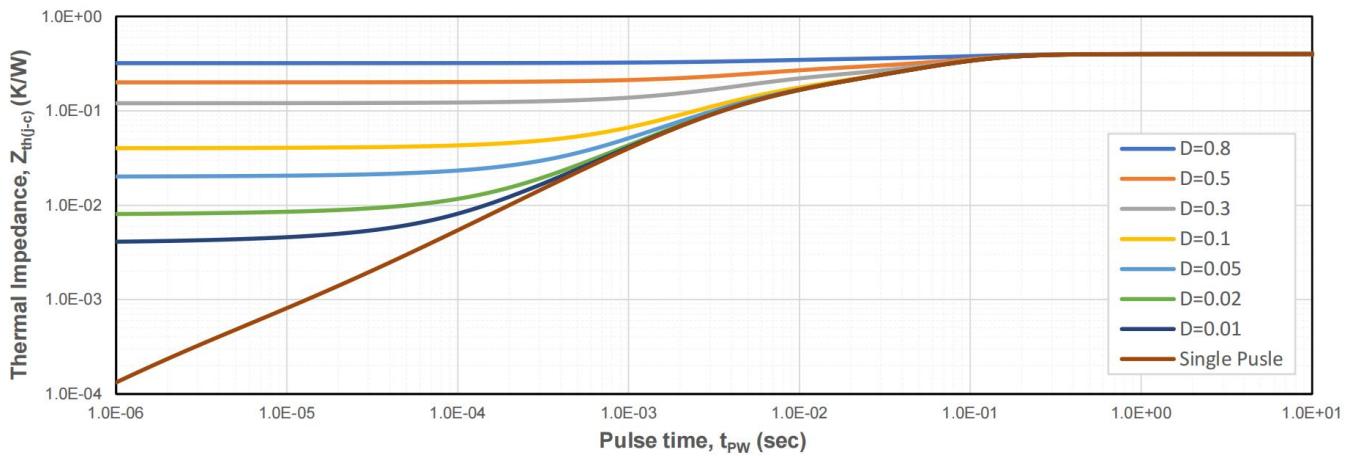


Figure 19. Transient Junction to Case Thermal Impedance

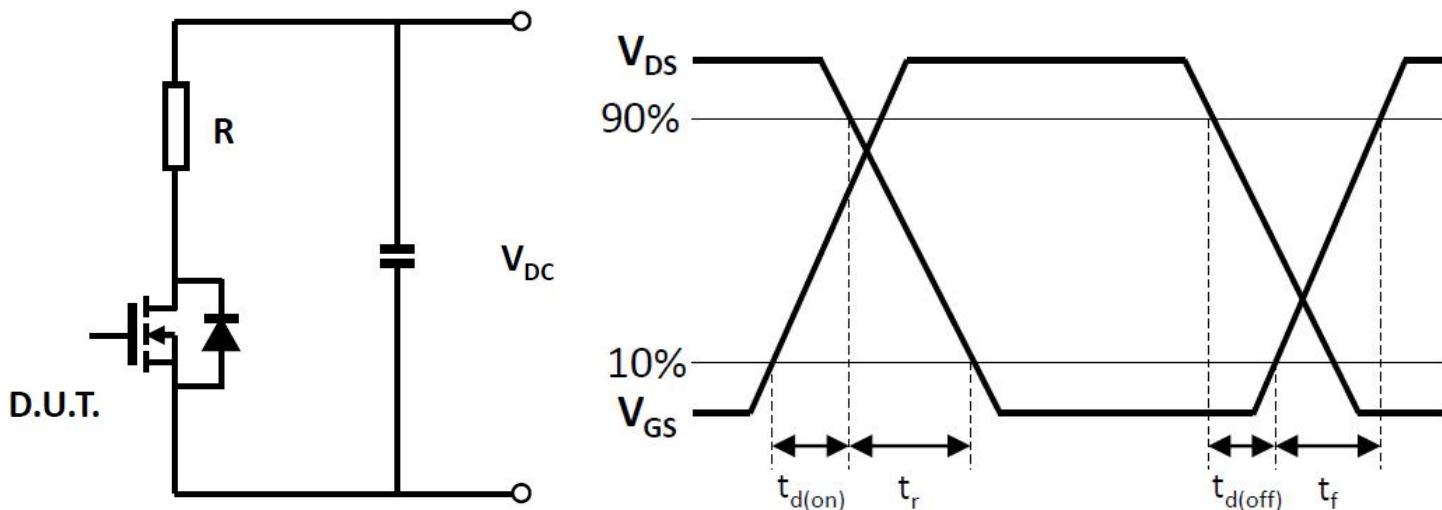
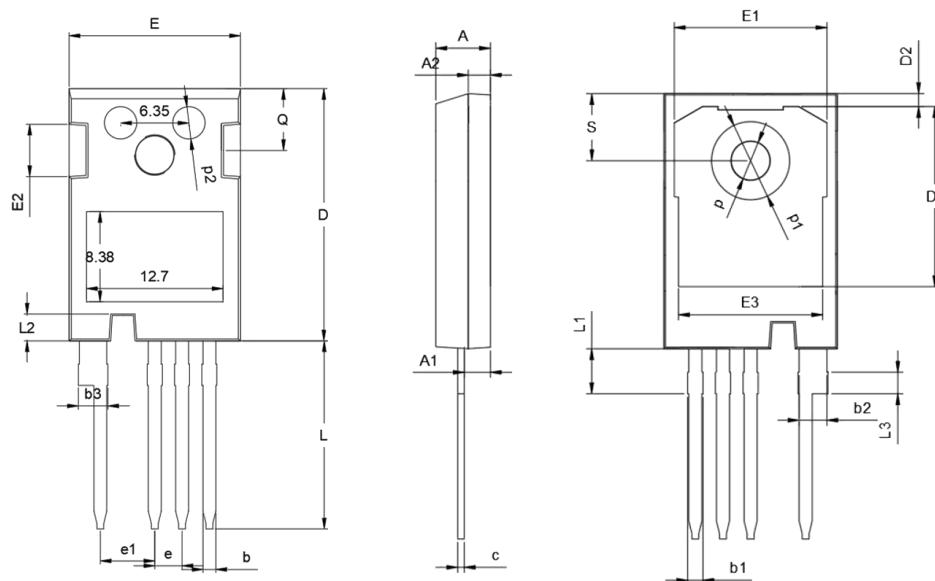


Figure 20. Schematic of Resistive Switching

Figure 21. Switching Times Definition



■Outline Dimensions



| TO247-4L | | | |
|----------|---------|-------|-------|
| Dim | Min | Norm | Max |
| A | 4.80 | 5.00 | 5.20 |
| A1 | 2.30 | 2.40 | 2.50 |
| A2 | 1.88 | 1.98 | 2.08 |
| b | 1.10 | 1.20 | 1.30 |
| b1 | 1.20 | / | 1.50 |
| b2 | 2.35 | 2.55 | 2.75 |
| b3 | 2.45 | / | 2.85 |
| c | 0.55 | 0.60 | 0.65 |
| D | 23.3 | 23.45 | 23.6 |
| D1 | 16.25 | 16.55 | 16.85 |
| D2 | 1.00 | / | 1.30 |
| e | TYP2.54 | | |
| e1 | TYP5.06 | | |
| E | 15.75 | 15.90 | 16.05 |
| E1 | 13.80 | / | 14.20 |
| E2 | 4.40 | 4.75 | 5.10 |
| E3 | 13.00 | / | 13.45 |
| L | 17.34 | 17.49 | 17.64 |
| L1 | 4.00 | / | 4.30 |
| L2 | 2.35 | / | 2.65 |
| L3 | TYP1.98 | | |
| Q | 5.60 | 5.80 | 6.00 |
| S | 6.05 | / | 6.30 |
| p | TYP3.58 | | |
| p1 | TYP7.18 | | |
| p2 | TYP3.00 | | |



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